

# KYLE RIETTA

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## Summary

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Electrical Engineer with over 6 years of hands-on experience in quality & performance analysis, hardware prototype testing and new product development. Collaborative team player with a strong track record of system troubleshooting, prototype validation, failure analysis, PCB reworks and solution implementation.

## Education

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### Project Management Institute

April 2026

*Certified Associate of Project Management (CAPM)*

### California State University - Northridge

Sept 2018 – May 2022

*Bachelor of Science, Electrical Engineering*

*Northridge, California*

- AIMS<sup>2</sup> Hispanic Serving Institution STEM Grant Program Award Member

## Professional Experience

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### Abbott Laboratories - Cardiac Rhythm Management

Aug 2026 - Present

*Electrical Engineer - Quality & Product Performance*

*Sylmar, California*

- Conduct post-market quality & performance analysis on explanted medical devices including cutting edge leadless pacemakers, transvenous pacemakers and implantable cardiac monitors.
- Perform electrical and functional evaluations to assess device behavior and identify potential failure mechanisms in returned products.
- Document technical findings and communicate results to engineering and quality teams to support product reliability investigations and design improvements.
- Collaborate with global cross-functional teams including hardware engineering, software engineering, manufacturing, market surveillance, and customer support to review field performance data.
- Support root-cause investigations by correlating device analysis results with field reports, manufacturing history, and product design information.
- Prepare detailed technical reports summarizing analysis methods, observations, and conclusions for both customers and internal engineering and quality stakeholders.

### Alcatel-Lucent Enterprise

Dec 2021 - Aug 2026

*Hardware Engineer*

*Thousand Oaks, California*

- Collaborate with a multidisciplinary and international team of engineers, business professionals, and FAEs from leading chip companies to ensure a cohesive approach to hardware development and deployment to a global market.
- Consistently meet project timelines and deadlines to transition from prototype to production build and deployment to the field by leveraging efficient hardware testing and thorough design reviews.
- Evaluate prototype products, ensuring design quality, integrity and functionality by performing rigorous test and validation processes and providing feedback for design modifications and improvements.
- Demonstrate excellent communication skills by effectively bridging the gap between international and interdepartmental team members, both technical and non-technical, through clear and concise documentation, regular updates, and proactive engagement.
- Participate in the complete life cycle of hardware projects, from design review, initial troubleshooting and root-cause analysis to successful solution deployment of network switch hardware.
- Ensure projects stayed on budget by managing costs, optimizing the use of testing equipment, negotiating favorable rates from vendors, and identifying cost-saving opportunities without sacrificing quality or performance.
- Actively involved in hands-on tasks, including soldering and circuit analysis, to ensure optimal hardware performance and customer satisfaction.

### Universal Studios Hollywood - Technical Services

Sept 2017 – Jan 2019

*Control Engineer Intern*

*Universal City, California*

- Lead a project to facilitate a complete control system upgrade which includes show control and ride vehicle motion.
- Analyzed existing documentation and implemented safe and efficient method to execute initial phase of the project.
- Collaborated with the team of engineers to design a new ride control system architecture and completed documentation.
- Assisted team with Factory and Site Acceptance Testing to demonstrate complete and safe operation of equipment.

## Technical Skills

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**Technical Tools:** Oscilloscopes, I2C/SPI Analyzers, TI Fusion Designer, Xpedition PCB, Lattice Diamond, PSPICE, Linux

**Languages:** Python, LaTeX, MATLAB

**Software:** Oracle Agile PLM, YouTrack PM, Microsoft Excel, Word, PowerPoint, Outlook, Teams, Zoom, EPIQ